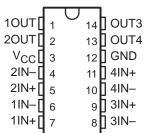
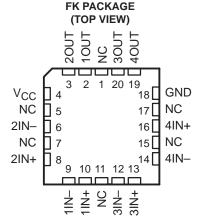
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage ... 2 V to 36 V
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ
- Low Input Bias Current . . . 25 nA Typ
- Low Input Offset Current . . . 3 nA Typ (LM139)
- Low Input Offset Voltage . . . 2 mV Typ
- **Common-Mode Input Voltage Range Includes Ground**
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and
- **Package Options Include Plastic** Small-Outline (D, NS), Shrink Small-Outline (DB), Thin Shrink Small-Outline (PW), and Ceramic Dual Flatpack (W) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) DIPs

D, DB, J, N, NS, PW, OR W PACKAGE (TOP VIEW)





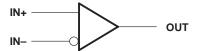
NC - No internal connection

description

These devices consist of four independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as long as the difference between the two supplies is 2 V to 36 V and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wire-AND relationships.

The LM139 and LM139A are characterized for operation over the full military temperature range of -55°C to 125°C. The LM239 and LM239A are characterized for operation from -25°C to 125°C. The LM339 and LM339A are characterized for operation from 0°C to 70°C. The LM2901 is characterized for operation from -40°C to 125°C.

symbol (each comparator)





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

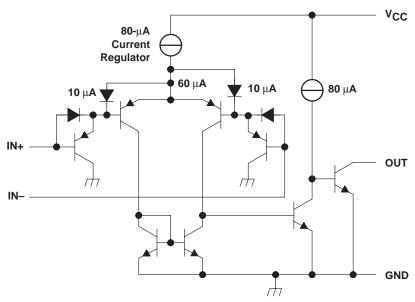


AVAILABLE OPTIONS

				PAC	KAGED DEV	ICES		
TA	V _{IO} max at 25°C	PLASTIC PLASTIC SOIC SSOP (D, NS)† (DB)‡		CHIP CERAMIC PLASTIC PLASTIC SSOP (DB)‡ (FK) CERAMIC PLASTIC PLASTIC TSSOP (PW)‡		CERAMIC DUAL FLATPACK (W)		
0°C to 70°C	5 mV 5 mV 2 mV 2 mV	LM339D LM339NS LM339AD LM339ANS	LM339DBR — — —	_	_	LM339N — LM339AN —	LM339PWR — — —	_
–25°C to 85°C	5 mV 2 mV	LM239D LM239AD	_	_	_	LM239N LM239AN	_	_
-40°C to 125°C	7 mV 7 mV	LM2901D LM2901NS	LM2901DBR	_	_	LM2901N	LM2901PWR	_
−55°C to 125°C	5 mV 2 mV	LM139D LM139AD	_	LM139FK LM139AFK	LM139J LM139AJ	_	_	LM139W LM139AW

[†]The D and NS packages are available taped and reeled. Add the suffix R to the device type (e.g., LM339DR).

schematic (each comparator)



All current values shown are nominal.



[‡]The DB and PW packages are only available taped and reeled.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V_{CC} (see Note 1)		±36 V –0.3 V to 36 V
Output current, IO		
Duration of output short circuit to ground (see I		
Package thermal impedance, θ _{JA} (see Note 4)): D package	86°C/W
	DB package	96°C/W
	N package	80°C/W
	NS package	
	PW package	
Continuous total dissipation		See Dissipation Rating Table
Case temperature for 60 seconds: FK package	e	
Lead temperature 1,6 mm (1/16 inch) from cas	se for 10 seconds: D, DB, N,	or PW package 260°C
Lead temperature 1,6 mm (1/16 inch) from cas		
Storage temperature range, T _{stg}		

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to network ground.
 - 2. Differential voltages are at IN+ with respect to IN-.
 - 3. Short circuits from outputs to $V_{\hbox{\footnotesize{CC}}}$ can cause excessive heating and eventual destruction.
 - 4. The package thermal impedance is calculated in accordance with JESD 51.

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING	T _A = 125°C POWER RATING
FK	900 mW	11 mW/°C	68°C	880 mW	715 mW	275 mW
J	900 mW	11 mW/°C	68°C	880 mW	715 mW	275 mW



LM139, LM139A, LM239, LM239A, LM339, LM339A, LM339Y, LM2901 QUAD DIFFERENTIAL COMPARATORS

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electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

	PARAMETER	TEOT 001	UDITIONO†	- +	L	M139		LN	1139A		UNIT
	PARAMETER	TEST CO	NDITIONS†	T _A ‡	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
V _{IO}	Input offset voltage	$V_{CC} = 5 \text{ V to}$ $V_{IC} = V_{ICR}(n)$		25°C		2	5		1	2	mV
VIO	input onset voltage	V _O = 1.4 V	,	Full range			9			4	IIIV
lio	Input offset current	V _O = 1.4 V		25°C		3	25		3	25	nA
lio	input onset current	v0 = 1.1 v	V() = 1.4 V				100			100	ПА
I _{IB}	Input bias current	V _O = 1.4 V		25°C		-25	-100		-25	-100	nA
ıв	input bias current	VO = 1.4 V	VO = 1.4 V				-300			-300	ПА
	Common-mode			25°C	0 to V _{CC} -1.5			0 to V _{CC} -1.5			
VICR	input-voltage range			Full range	0 to V _{CC} -2			0 to V _{CC} -2			V
AVD	Large-signal differential-voltage amplification	$V_{CC} \pm = \pm 7.5$ $V_{O} = -5 \text{ V to}$		25°C		200		50	200		V/mV
lau	High-level output	\/ 1 \/	V _{OH} = 5 V	25°C		0.1			0.1		nA
ЮН	current	V _{ID} = 1 V	V _{OH} = 30 V	Full range			1			1	μΑ
Va.	Low-level output	V _{ID} = -1 V,	lo: - 4 mΛ	25°C		150	400		150	400	mV
VOL	voltage	$V \mid D = -1 V,$	$I_{OL} = 4 \text{ mA}$	Full range			700			700	IIIV
lOL	Low-level output current	V _{ID} = −1 V,	V _{OL} = 1.5 V	25°C	6	16		6	16		mA
Icc	Supply current (four comparators)	V _O = 2.5 V,	No load	25°C		0.8	2		0.8	2	mA

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST CON	IDITIONS	L	UNIT		
		MIN	TYP	MAX		
R _L con	R _L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3			
Response time	C _L = 15 pF§, See Note 5	TTL-level input step		0.3		μs

[§] C_L includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



[‡] Full range (MIN to MAX) for LM139 and LM139A is -55°C to 125°C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

	PARAMETER	TEST COM	NDITIONS†	T _A ‡		M239 M339			//239A //339A		UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
\/	Input offset voltage	$V_{CC} = 5 \text{ V to}$ $V_{IC} = V_{ICR}(m)$		25°C		2	5		1	3	mV
VIO	input onset voitage	$V_0 = V_0 = V_0$	III 1),	Full range			9			4	IIIV
lio.	Input offset current	V _O = 1.4 V		25°C		5	50		5	50	nA
lio	input onset current	VO = 1.4 V	70 - 1.4 V				150			150	ША
l.s	Input bias current	\/o = 1 4 \/	'O = 1.4 V			-25	-250		-25	-250	nA
IB	input bias current	VO = 1.4 V		Full range			-400			-400	IIA
Common-mode				25°C	0 to V _{CC} -1.5			0 to V _{CC} -1.5			
VICR	input-voltage range			Full range	0 to V _{CC} -2			0 to V _{CC} -2			·
AVD	Large-signal differential-voltage amplification	V_{CC} = 15 V, V_{O} = 1.4 V to $R_{L} \ge$ 15 k Ω to		25°C	50	200		50	200		V/mV
1	High-level output	\/ 4\/	V _{OH} = 5 V	25°C		0.1	50		0.1	50	nA
ЮН	current	V _{ID} = 1 V	V _{OH} = 30 V	Full range			1			1	μΑ
\/-·	Low-level output	\/ 4\/	1 m A	25°C		150	400		150	400	mV
VOL	voltage	$V_{ID} = -1 V$,	$I_{OL} = 4 \text{ mA}$	Full range			700			700	IIIV
loL	Low-level output current	V _{ID} = −1 V,	V _{OL} = 1.5 V	25°C	6	16		6	16		mA
	Cumply ourrant	V _O = 2.5 V,	No load			0.8	2		0.8	2	
Icc	Supply current (four comparators)	V _{CC} = 30 V, No load	V _O = 2.5 V,	25°C							mA

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER	TEST CON	IDITIONS	LM23 LM33	UNIT		
			MIN	TYP	MAX	
Response time	R _L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive		1.3		
Response time	C _L = 15 pF§, See Note 5	TTL-level input step		0.3		μs

§ C_L includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



Full range (MIN to MAX) for LM239 and LM239A is -25°C to 85°C, for LM339 and LM339A is 0°C to 70°C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

LM139, LM139A, LM239, LM239A, LM339, LM339A, LM339Y, LM2901 QUAD DIFFERENTIAL COMPARATORS

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electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

	DADAMETED			_ +	LN	/12901		UNIT
	PARAMETER	TEST CC	ONDITIONS†	T _A ‡	MIN	TYP	MAX	UNII
V	Input offset voltage	$V_{CC} = 5 \text{ V to } 30 \text{ V},$ $V_{IC} = V_{ICR}(\text{min}),$		25°C		2	7	mV
VIO	input onset voltage	$V_O = 1.4 \text{ V}$		Full range			15	IIIV
lio	Input offset current	V _O = 1.4 V		25°C		5	50	nA
lio	input onset current	VO = 1.4 V		Full range			200	1,,,
lin.	Input bias current V _O = 1.4 V			25°C		-25	-250	nA
[†] IB	input bias current	VO = 1.4 V		Full range			-500	IIA
\\\.	Common-mode input-voltage		25°C		0 to V _{CC} -1.5			٧
VICR	range			Full range	0 to V _{CC} -2			
AVD	Large-signal differential-voltage amplification	$V_{CC} = 15 \text{ V},$ $V_{O} = 1.4 \text{ V to } 11.4 \text{ V},$ $R_{L} \ge 15 \text{ k}\Omega \text{ to } V_{CC}$	/,	25°C	25	100		V/mV
1	High lovel output ourrent	\/ 1\/	V _{OH} = 5 V	25°C		0.1	50	nA
ЮН	High-level output current	V _{ID} = 1 V	V _{OH} = 30 V	Full range			1	μΑ
1/2.	Low lovel output voltage	V 1 V	la. – 4 mΛ	25°C		150	500	mV
VOL	Low-level output voltage	$V_{ID} = -1 V$,	$I_{OL} = 4 \text{ mA}$	Full range			700	IIIV
loL	Low-level output current	$V_{ID} = -1 V$,	V _{OL} = 1.5 V	25°C	6	16		mA
	Supply current	V _O = 2.5 V, No load				0.8	2	
ICC	(four comparators)	V _{CC} = 30 V, No load	V _O = 2.5 V,	25°C		1	2.5	mA

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER	TEST CON	L	UNIT			
PARAMETER	TEST CON	CHOILIGIS	MIN	TYP	MAX	UNIT
Decrease time	R _L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3			
Response time	C _L = 15 pF [§] , See Note 5	TTL-level input step		0.3		μs

[§] C_L includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



[‡] Full range (MIN to MAX) for LM2901 is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

electrical characteristics at V_{CC} = 5 V, T_A = 25°C (unless otherwise noted)

	PARAMETER	TEST CO	NDITIONS†	LN	1339Y		UNIT
	PARAMETER	lesi co	NDITIONS!	MIN	TYP	MAX	UNII
V _{IO}	Input offset voltage	$V_{CC} = 5 \text{ V to } 30 \text{ V},$ $V_{O} = 1.4 \text{ V}$	$V_{IC} = V_{ICR}(min),$		2	5	mV
IIO	Input offset current	V _O = 1.4 V			5	50	nA
I _{IB}	Input bias current	V _O = 1.4 V			-25	-250	nA
VICR	Common-mode input-voltage range			0 to V _{CC} –1.5			V
AVD	Large-signal differential-voltage amplification	$V_{CC} = 15 \text{ V},$ $R_L \ge 15 \text{ k}\Omega \text{ to } V_{CC}$	$V_O = 1.4 \text{ V to } 11.4 \text{ V},$	25	100		V/mV
ІОН	High-level output current	V _{ID} = 1 V,	V _{OH} = 5 V		0.1	50	nA
VOL	Low-level output voltage	$V_{ID} = -1 V$,	I _{OL} = 4 mA		150	400	mV
loL	Low-level output current	$V_{ID} = -1 V$,	V _{OL} = 1.5 V	6	16		mA
		$V_0 = 2.5 V$,	No load		0.8	2	
ICC	Supply current (four comparators)	V _O = 30 V, No load	V _O = 15 V,		1	2.5	mA

[†] All characteristics are measured with zero common-mode input voltage unless otherwise specified.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST CO	TEST CONDITIONS				UNIT
PARAMETER	1231 00	MIN TYP	MAX	UNII		
Deen see time	R _L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3			
Response time	C _L = 15 pF [‡] , See Note 5	TTL-level input step		0.3		μs

‡ C_L includes probe and jig capacitance.

NOTE 5: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.



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